

Fig. 1

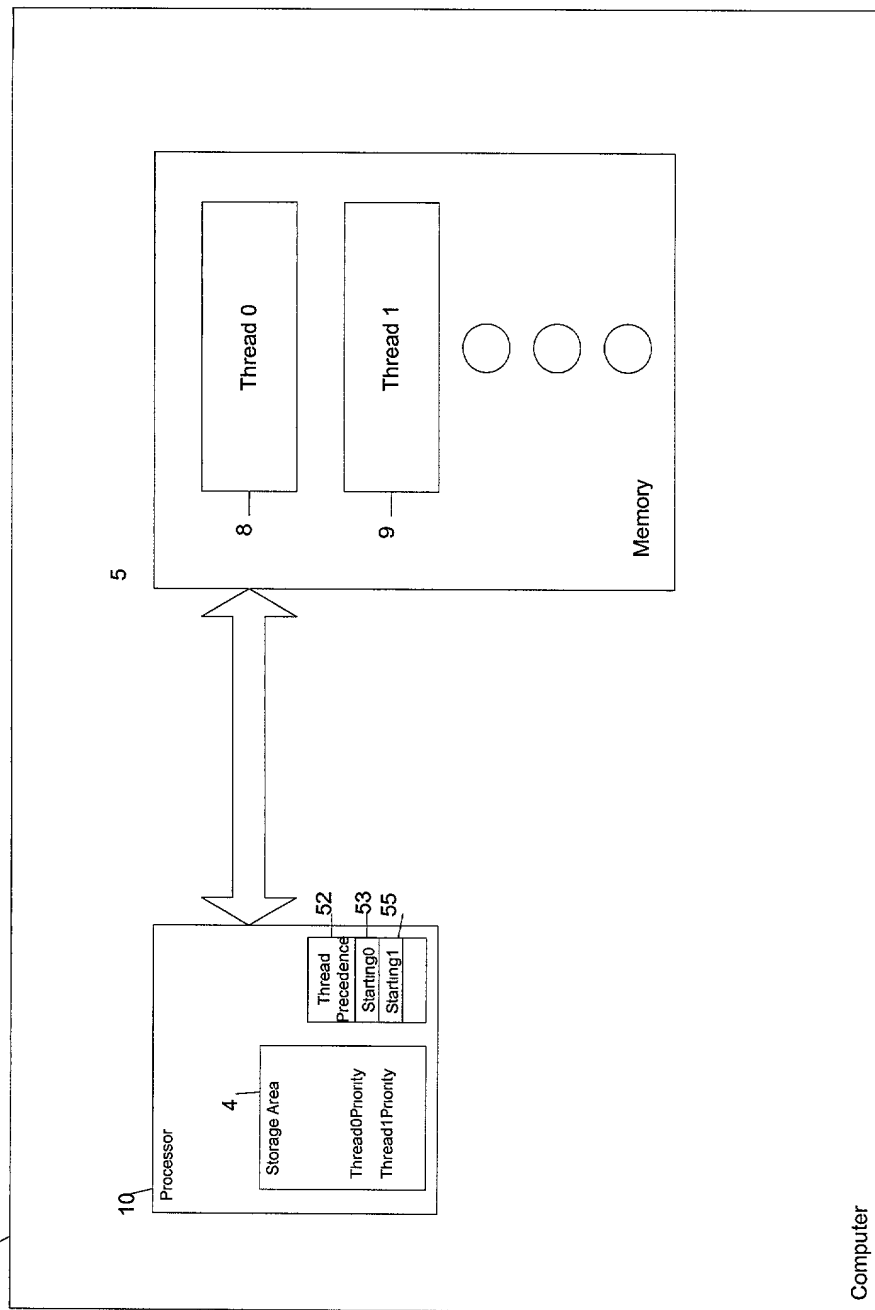


FIG. 1 is a block diagram of a computer system 1. The computer system 1 includes a processor 10 and a memory 5. The processor 10 includes a storage area 4. The storage area 4 includes thread0priority and thread1priority. The processor 10 also includes thread precedence starting0 52 and starting1 55. The memory 5 includes thread 0 8, thread 1 9, and three empty circles. A double-headed arrow indicates communication between the processor 10 and the memory 5.

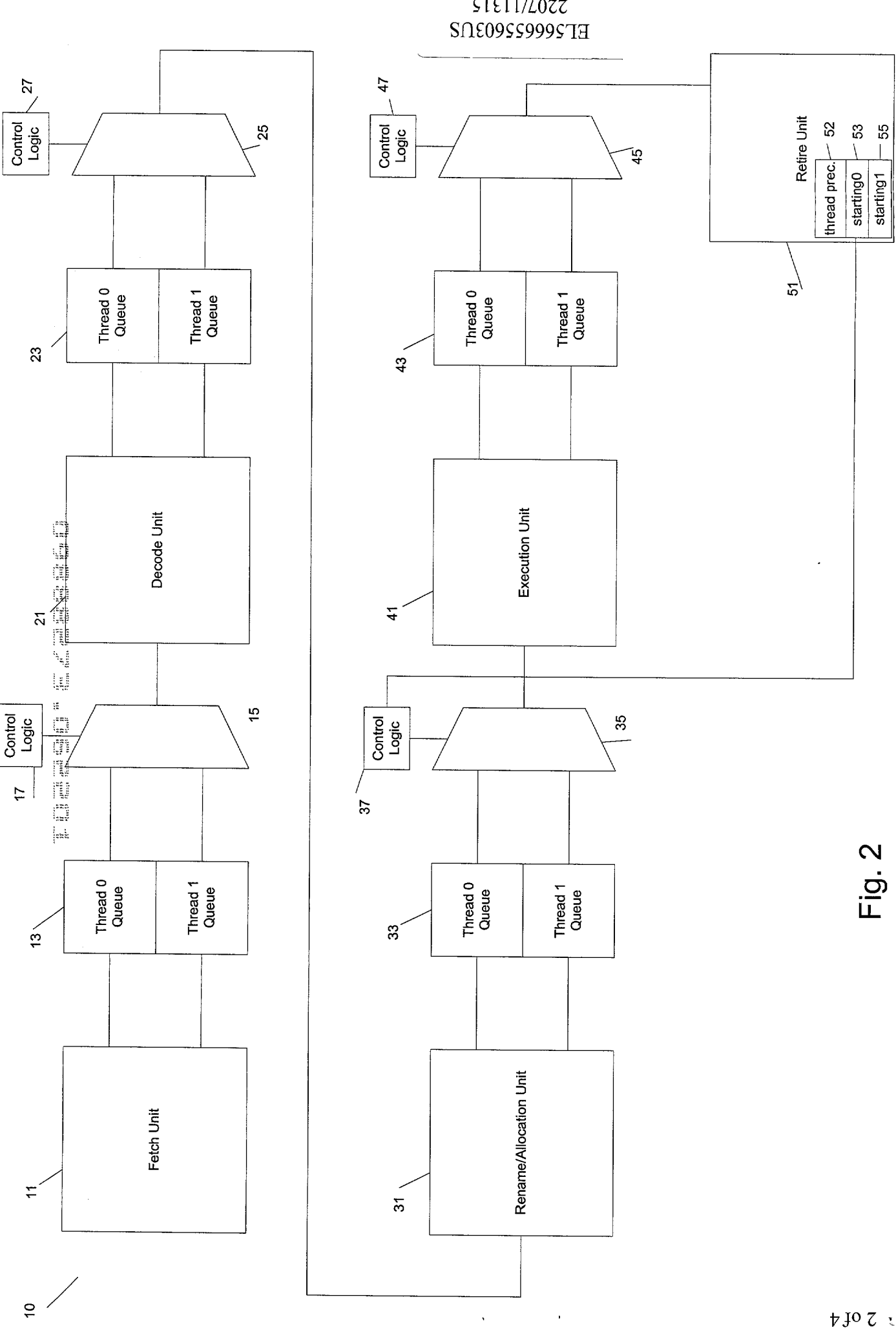


Fig. 2

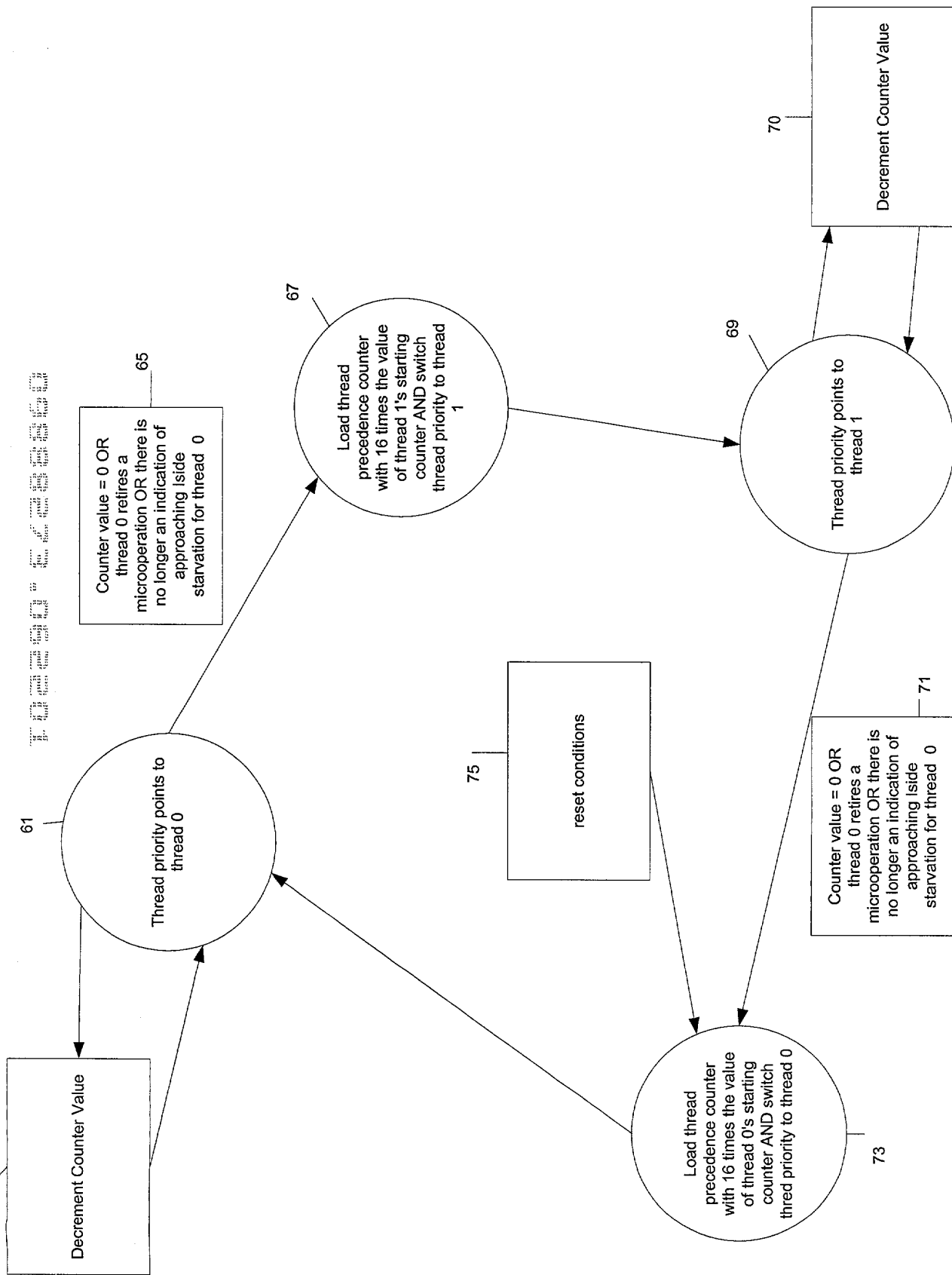


Fig. 3

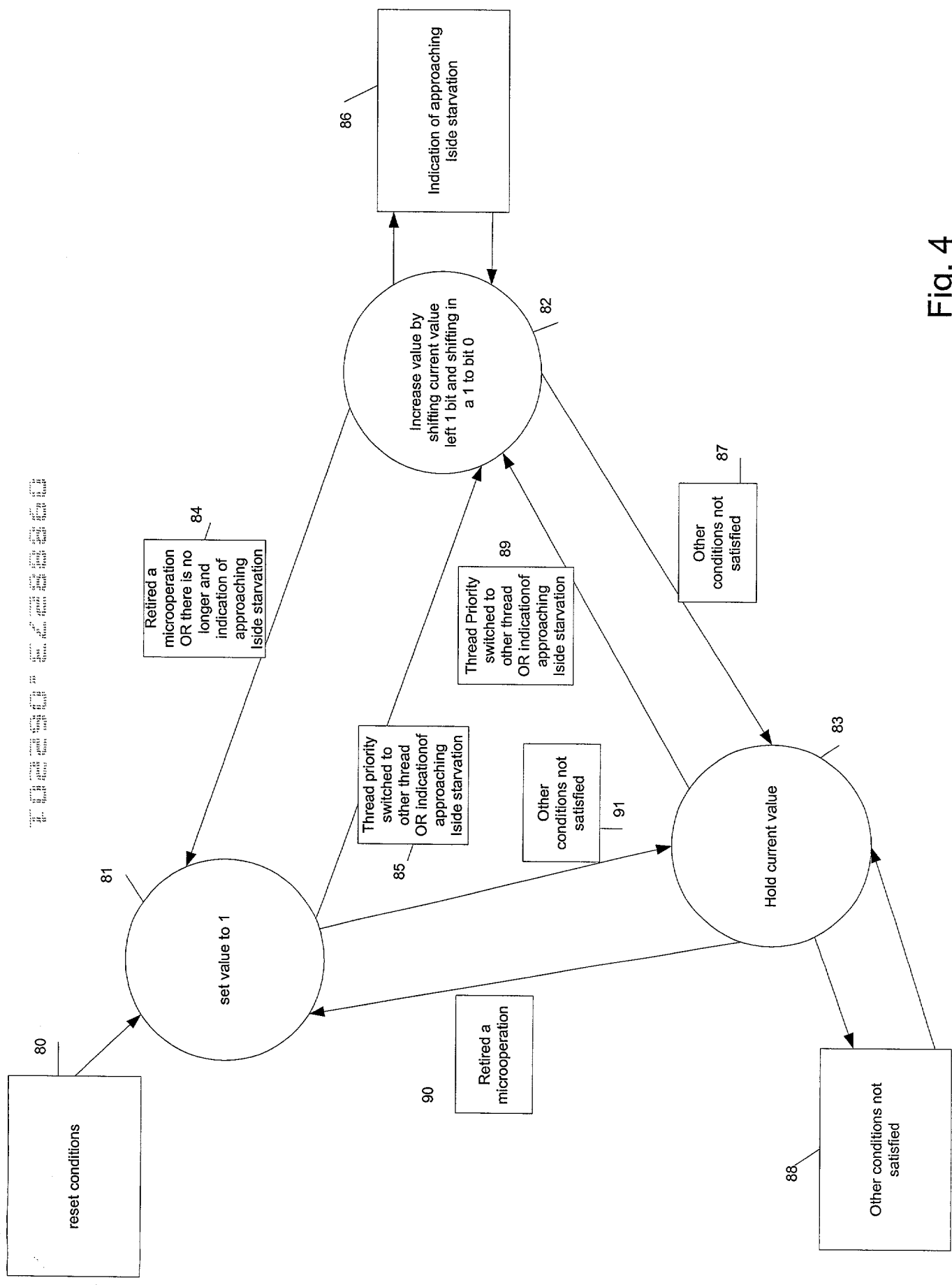


Fig. 4